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Applicant : Masato Yonezawa et al. Art Unit : Unknown
Serial No. : New Continuation Application Examiner : Unknown
Filed : February 11, 2004
Title : METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Under 35 USC §120, this application relies on the earlier filing date of application serial number 10/117,348, filed on April 8, 2002. The attached list of references were submitted to and/or cited by the Office in the prior application and, therefore, are not provided in this application.

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,


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Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Masato Yonezawa et al.					
		Filing Date February 11, 2004		Group Art Unit			

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Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
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Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No

Examiner Signature	Date Considered
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EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.